

COST Action no. IC1202

2013 | 2017

TACLe

Timing Analysis on Code-Level

Objectives

- To develop industrial strength code-level timing analysis (CLTA) techniques for future generation embedded systems (e.g., multi-core architectures)
- To further develop design principles for hard-/software and systems that improve timing predictability
- To increase the usability of timing analysis tools
- To promote the exchange of ideas with neighbouring research communities and to enhance coordination and interaction between European researchers and tool vendors in the field of CLTA

Working Groups

- WG 1: Timing Models For Multi-Cores and Timing Composability (Christine ROCHANGE)
- WG 2: Tooling Aspects (Martin SCHOEBERL)
- WG 3: Early-Stage Timing Analysis (Tullio VARDANEGA)
- WG 4: Resources Other Than Time (Stefan PETERS)

Main Achievements

- Administrative setup of the Action: Kick-off meeting held, STSM committee appointed, industrial advisory board set up
- First dissemination activities: Website online, special sessions already held and planned, joint publications launched
- Workshop on code-level timing analysis: Held in Paris, July 2013, organized by Claire MAIZA
- Working Group Activities:
High interest in multi-core issues, large potential for collaboration
Inventory of existing real-time code benchmarks prepared, joint search for further parallel benchmarks
Exchange of existing energy models foreseen

Gender Balance and Early Stage Researchers

- Objectives: To respect an appropriate gender balance and to considerably involve early stage researchers
- Status: 6% female researchers, 4 leading positions (MC members, WG lead, STSM committee chair) held by female scientists
13% early stage researchers
- Foreseen Support Measures: Promotion of involvement of female professionals in the Action's activities, particularly in leading positions (WG leader, MC member), active search for female scientists
Short-term scientific missions (10 budgeted), 2 focused meetings (Porto, Vienna) for early stage researchers

Dissemination

- 2 special sessions on TACLe held (ESWEEK, HiPEAC), 1 special session planned (HiPEAC fall meeting)
- 7 joint Papers accepted or submitted (ACM TECS, SEUS, EMSOFT, WCET)
- Industrial advisory board (Airbus, Bosch, Daimler, Rolls Royce)

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Communication
Technologies
(ICT)****Participating countries: 15**AT, DE, DK, EL, ES, FI, FR, IE, IT, LT,
MK, NO, PT, SE, UK**Internat. Collaboration:**

SG

Contact details

Chair of the ActionBjörn LISPER
Bjorn.Lisper@mdh.se**Vice Chair of the Action**Heiko FALK
Heiko.Falk@uni-ulm.de**Domain Committee Rapporteur**Gideon ARIELY
Gidi.Ariely@gmail.com**Science Officer (COST Office)**Ralph Stübner
Ralph.Stuebner@cost.eu**Website**www.tacle.eu**TACLe** 
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